Version	Date	Change Descripton
R0A	2014/07/01	This schematic leverage EAP1018FB-FLF-EC 5GHz RFcard
R01	2014/11/22	change text version to R01

11ac 3X3 Mini PCIe Card

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Model Name	Rev
OAP1122B_0614_EC	R01

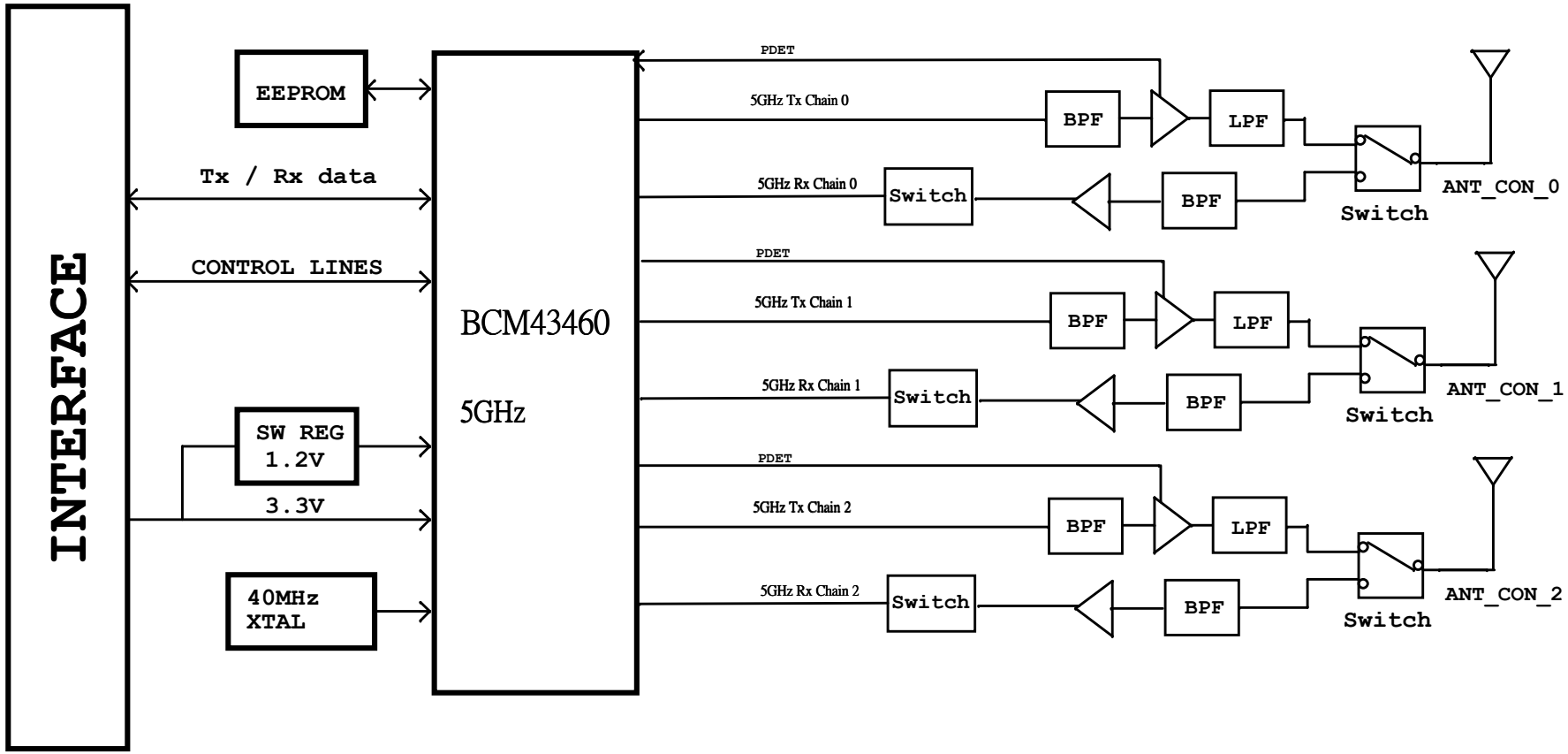
Size	Page Name	Engineer
C	Title Page	Erick Chen

Date: Thursday, March 12, 2015 Sheet 1 of 7

: POWER AMPLIFIER

: LOW NOISE AMPLIFIER

BPF : BAND PASS FILTER
PDET : POWER DETECTOR
ANT_CON : ANTENNA CONNECTOR

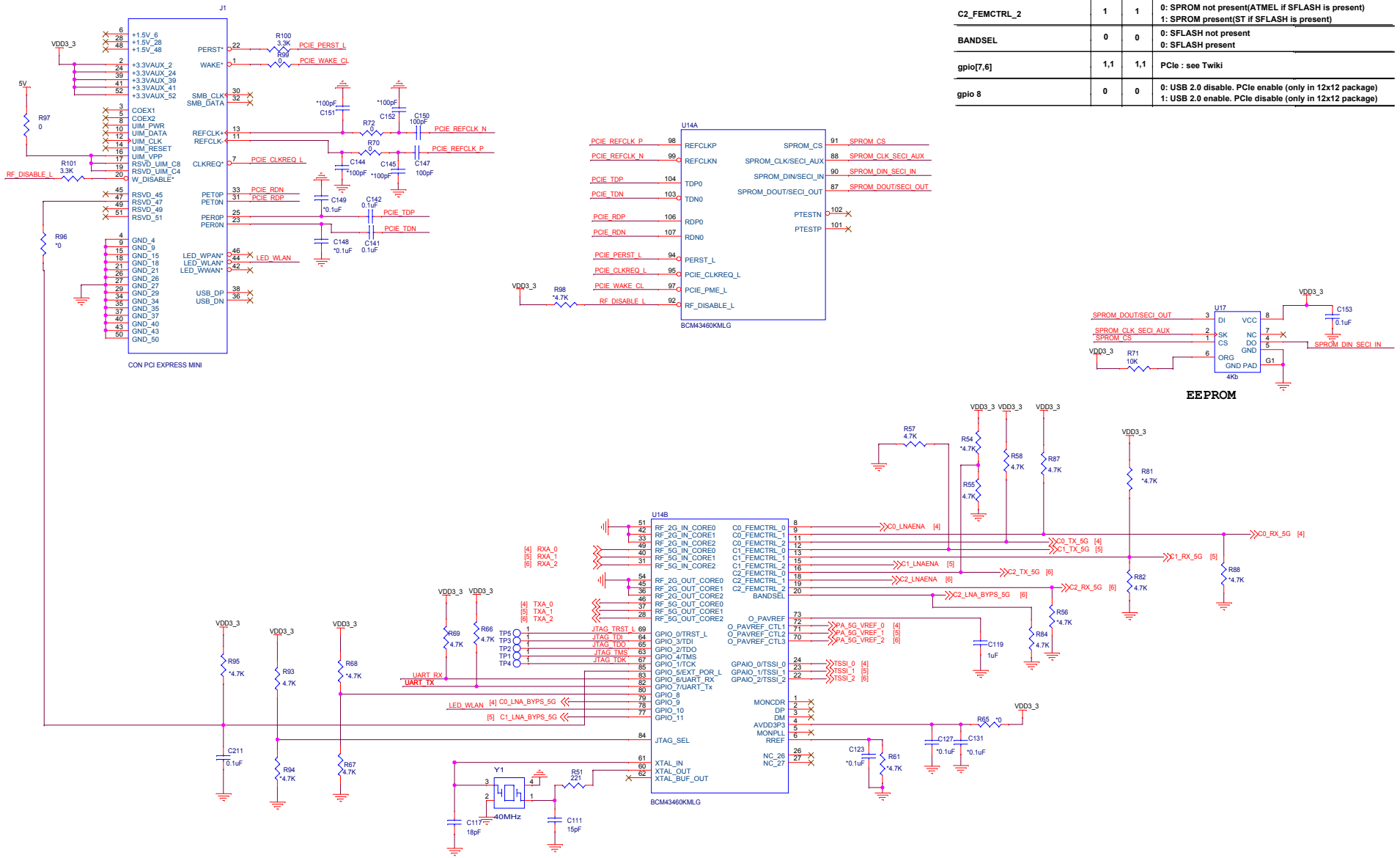


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Model Name	OAP1122B_0614_EC	Rev	R01
Size	C	Page Name	Block Diagram
Engineer	Erick Chen		
Date:	Thursday, March 12, 2015	Sheet	2 of 7

Pin	chip default	board default	Description
C0_FEMCTRL_1	1	1	0: 20MHz XTAL selected 1: 40MHz XTAL selected
C1_FEMCTRL_0, C0_FEMCTRL_2	0,1	0,1	0,1 : 4K SPROM size 1,0 ; 16K SPROM size
C1_FEMCTRL_1	1	0	0: Boot from SRAM 1: Boot from ROM
C2_FEMCTRL_0	0	0	0: don't query OTP 1: query OTP
C2_FEMCTRL_2	1	1	0: SPROM not present(ATMEL if SFLASH is present) 1: SPROM present(ST if SFLASH is present)
BANDSEL	0	0	0: SFLASH not present 0: SFLASH present
gpio[7,6]	1,1	1,1	PCIe : see Twiki
gpio 8	0	0	0: USB 2.0 disable. PCIe enable (only in 12x12 package) 1: USB 2.0 enable. PCIe disable (only in 12x12 package)



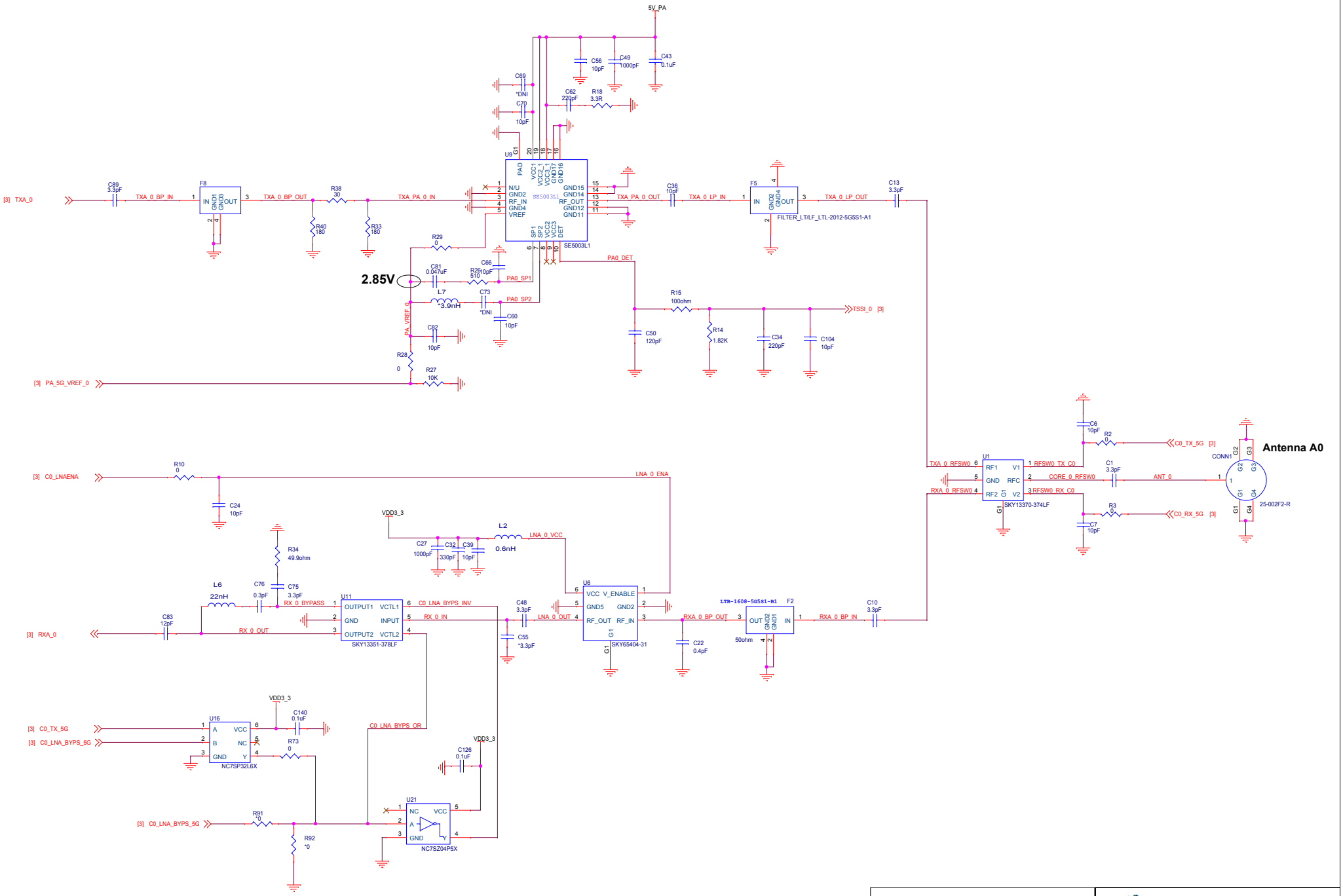
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Model Name	Rev
OAP1122B_0614_EC	R01

Size	Page Name	Engineer
C	PCIe Control Line	Erick Chen

Date: Thursday, March 12, 2015 Sheet 3 of 7



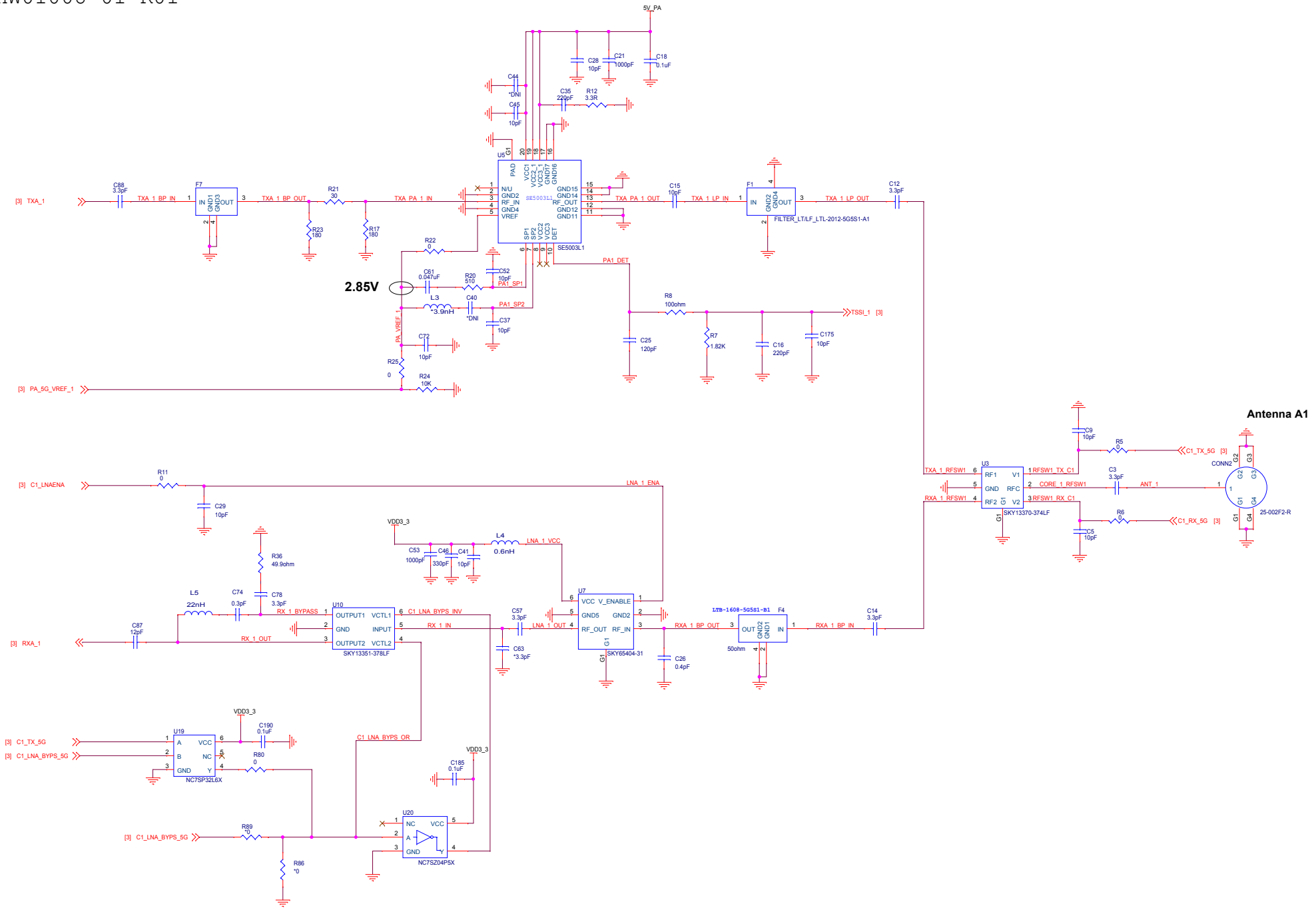
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Model Name	Rev
OAP1122B_0614_EC	R01

Size	Page Name	Engineer
C	RF front end chain0	Erick Chen

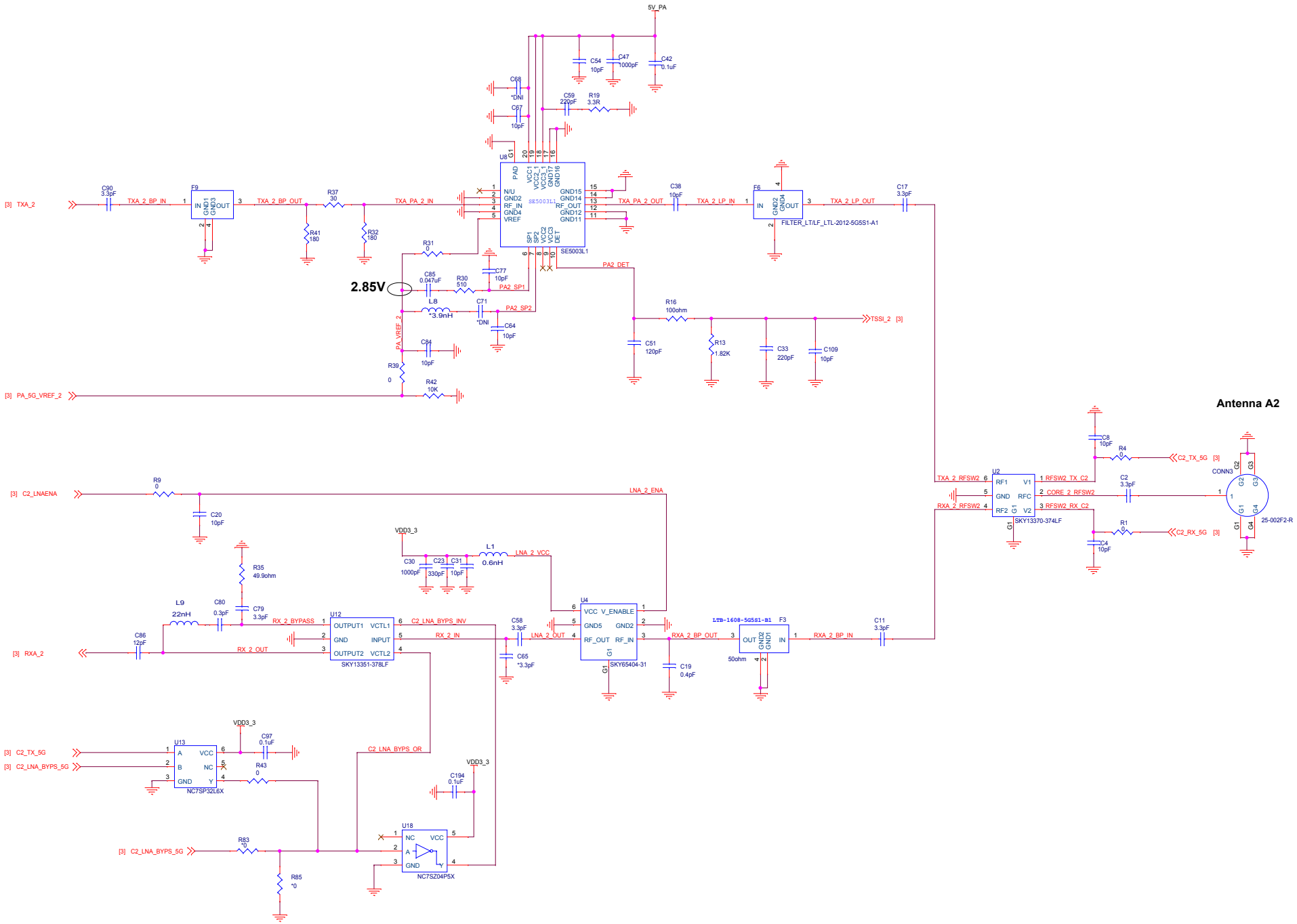
Date: Thursday, March 12, 2015 Sheet 4 of 7



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Model Name: OAP1122B_0614_EC		Rev: R01
Size: C	Page Name: RF front end core1	Engineer: Erick Chen
Date: Thursday, March 12, 2015	Sheet 5 of 7	



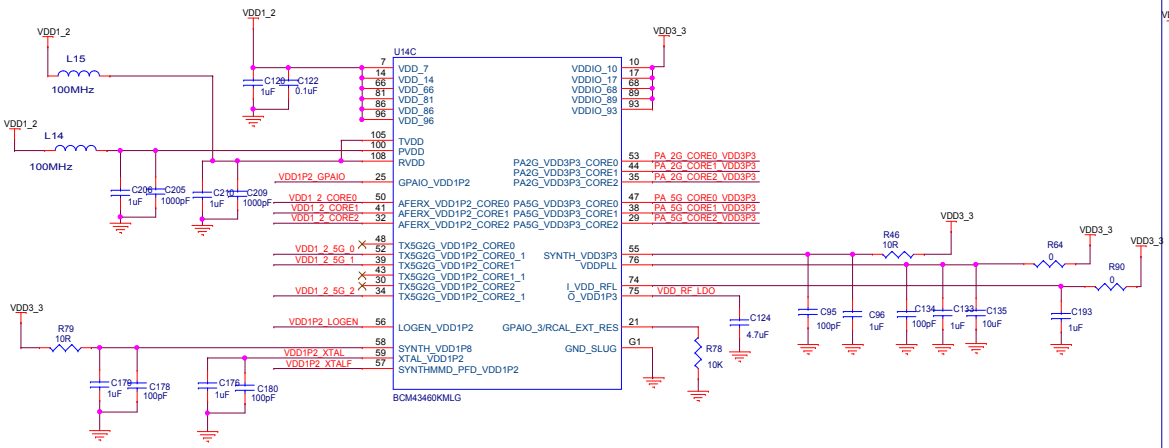
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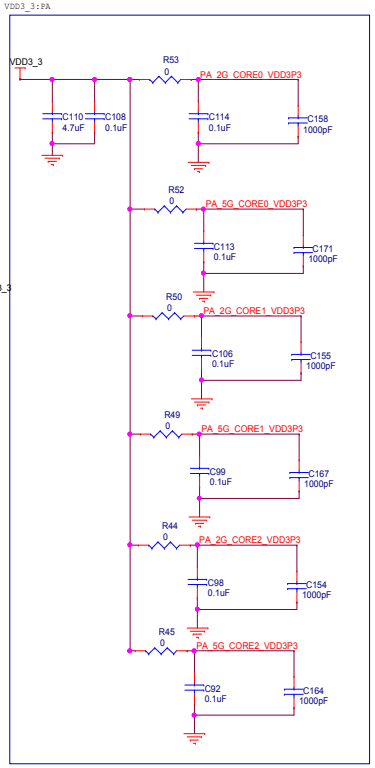
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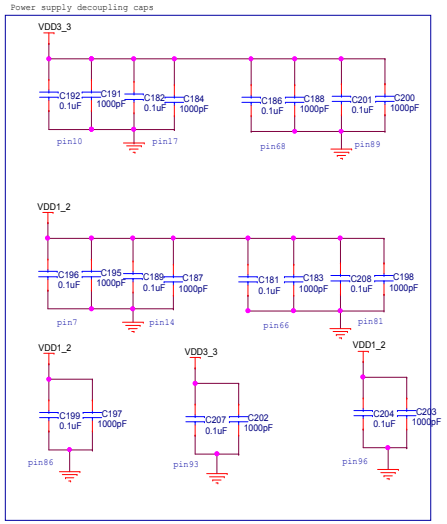
Model Name	Rev
OAP1122B_0614_EC	R01

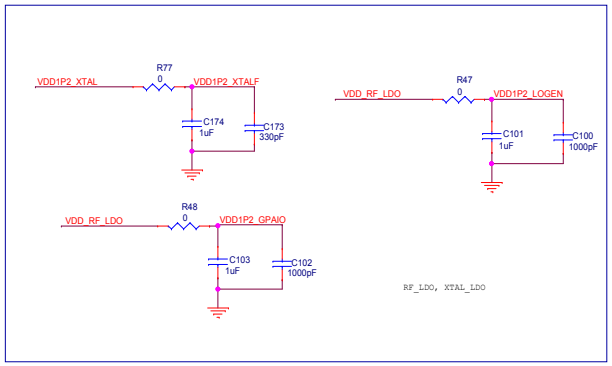
Size	Page Name	Engineer
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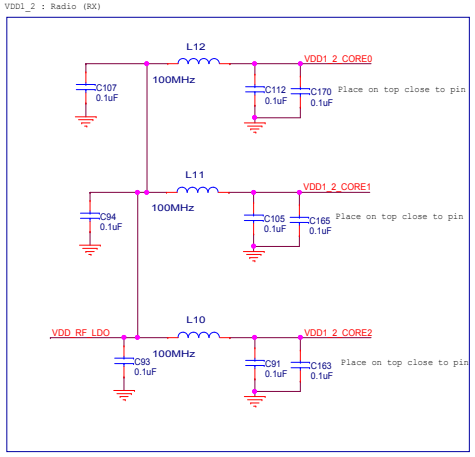
Date: Thursday, March 12, 2015 Sheet 6 of 7

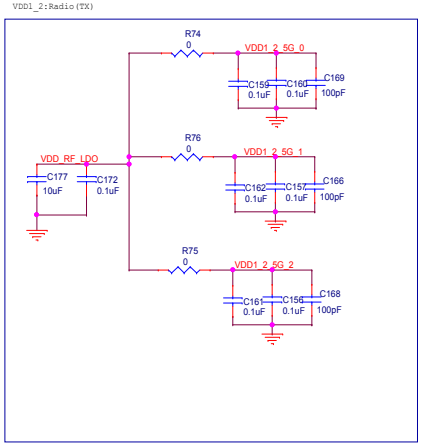


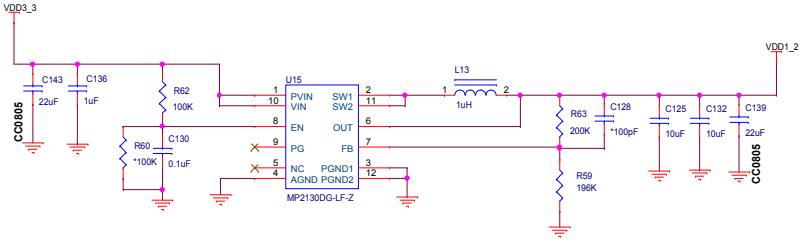


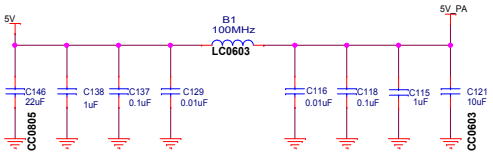


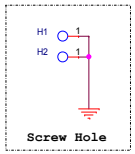


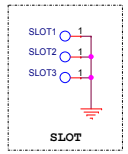
















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Model Name	Rev
OAP1122B_0614_EC	R01

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C	Power	Erick Chen

Date: Thursday, March 12, 2015 Sheet 7 of 7